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AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116 – EXPEDITED PROCEDURE

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Title: SHUNTING ARRANGEMENTS TO REDUCE HIGH CURRENTS IN GRID ARRAY CONNECTORS

Assignee: Intel Corporation

AF/ 2833

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IN THE ABSTRACT

PLEASE amend the Abstract as follows:

Abstract of the Disclosure

A shunt ~~shunt/mechanical~~ connector is provided to electrically couple ~~directly contact~~ two discrete electrical components mountable together via a grid array. The connector may ~~provide~~ provides mechanical support and provides a shunt electrical conduction path to increase ~~current-carrying capacity for predetermined electrical current~~ between the electrical components such that the ~~predetermined electrical current does not pass through grid array connectors of the~~ grid array. In an embodiment, at [[At]] least part of the shunt ~~shunt/mechanical~~ connector may ~~may~~ [[to]] extend within one of the electrical components so as to provide the shunt electrical conduction path.

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